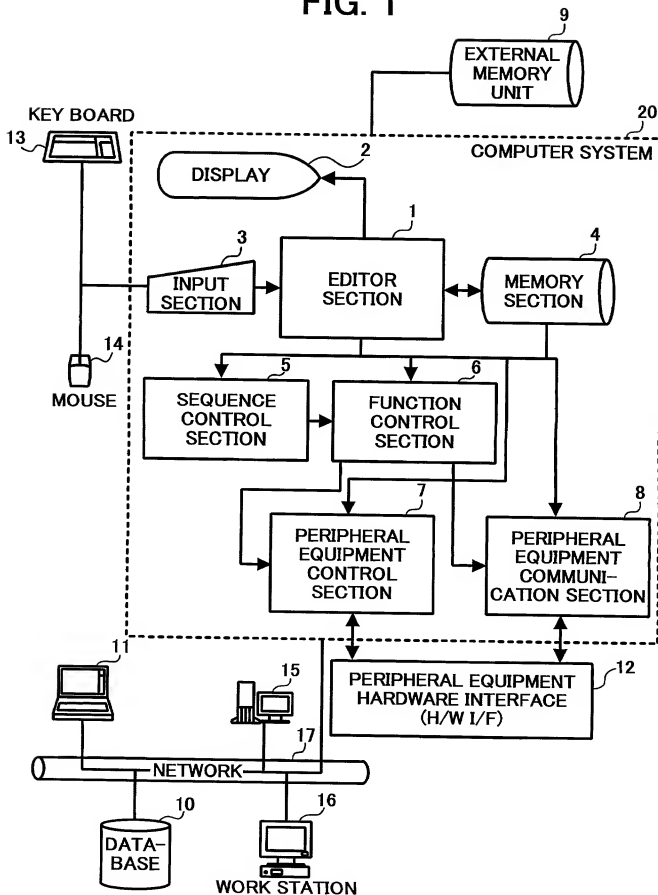


FIG. 1



10015800.121701

Figure 1. Schematic representation of the experimental design. The subjects were divided into two groups: the control group (CG) and the experimental group (EG). The CG was divided into two subgroups: the control group (CG) and the control group (CG). The EG was divided into two subgroups: the experimental group (EG) and the experimental group (EG). The CG was divided into two subgroups: the control group (CG) and the control group (CG). The EG was divided into two subgroups: the experimental group (EG) and the experimental group (EG).

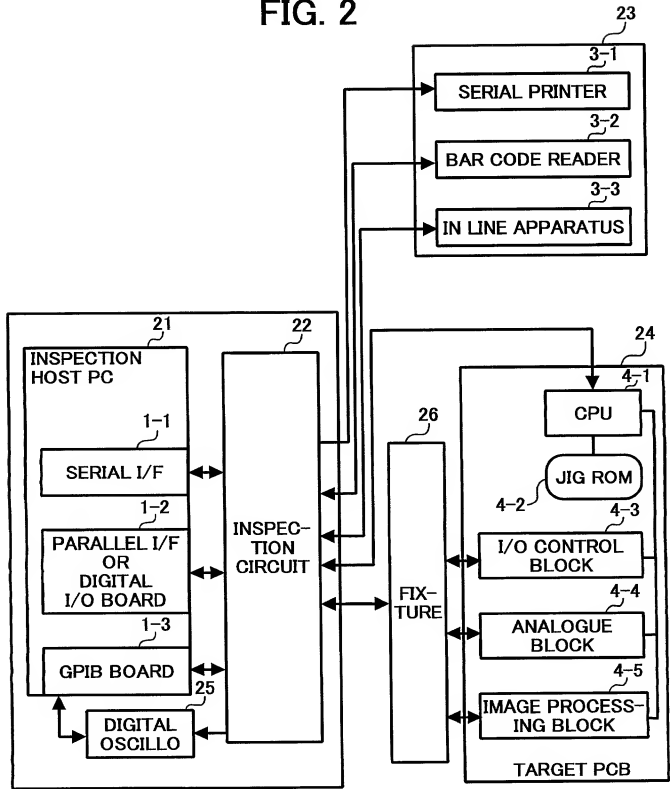


FIG. 3	FIG. 3A
	FIG. 3B
	FIG. 3C

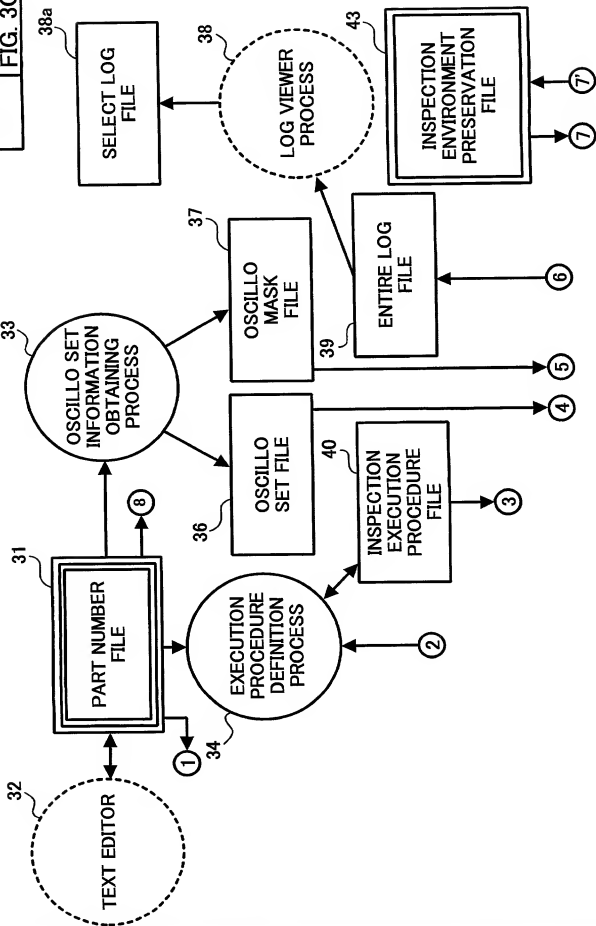


FIG. 3B

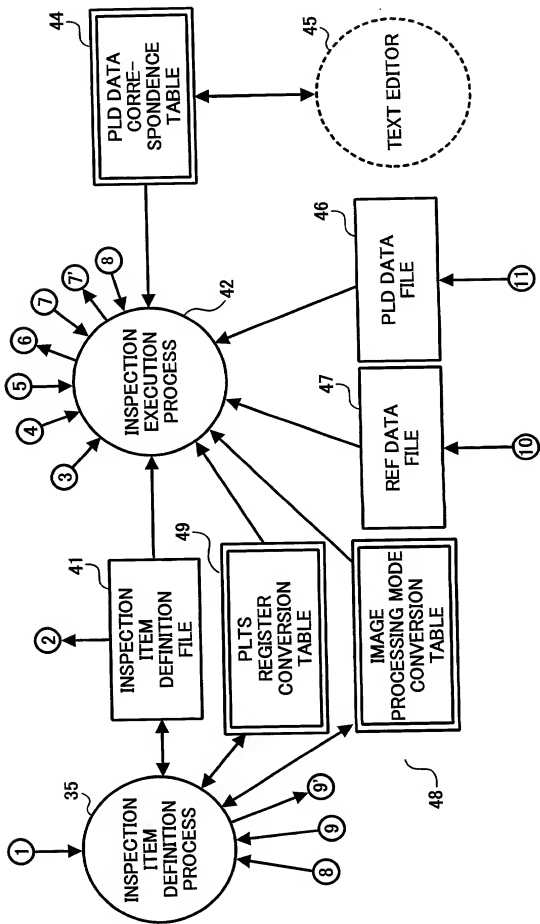


FIG. 3C

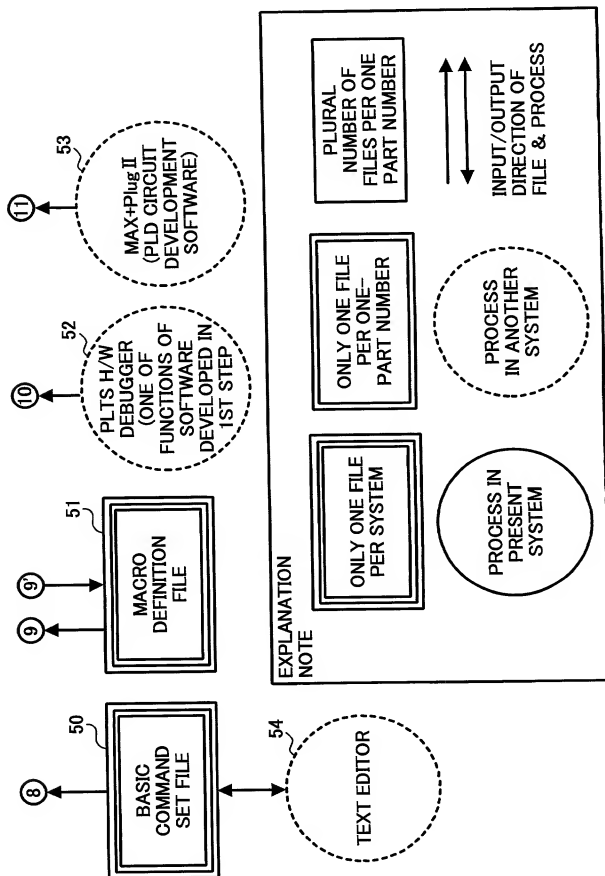


FIG. 4A

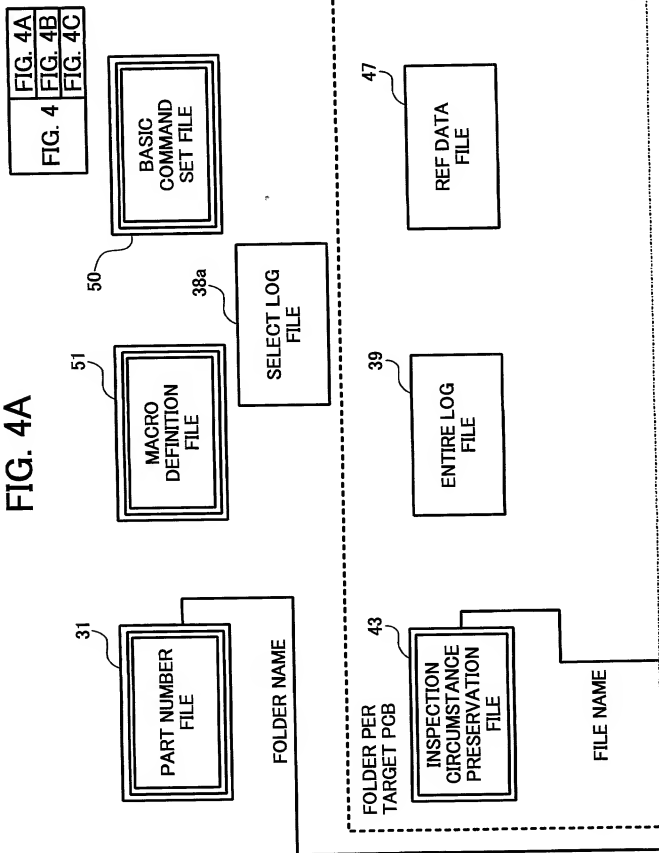


FIG. 4B

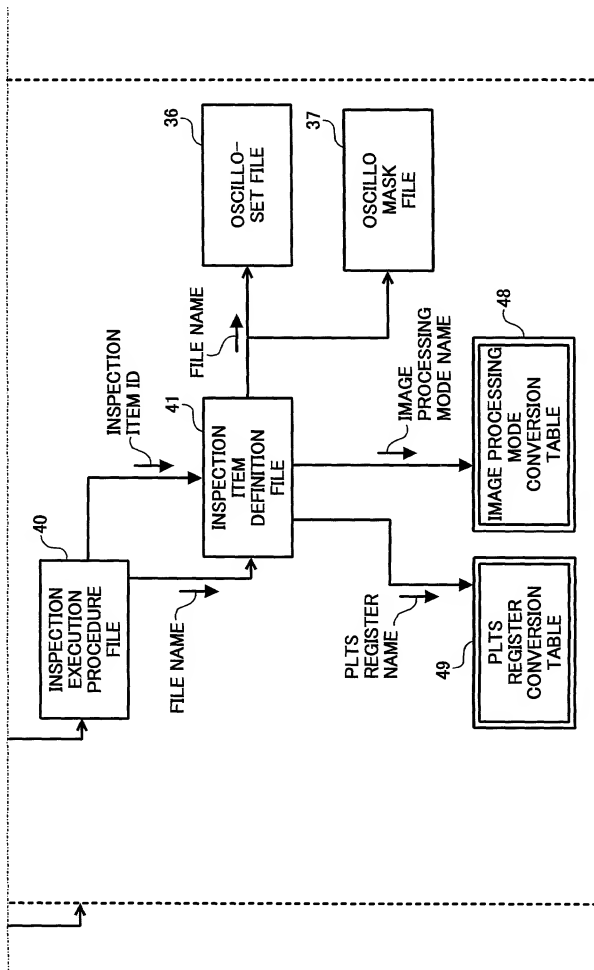


FIG. 4C

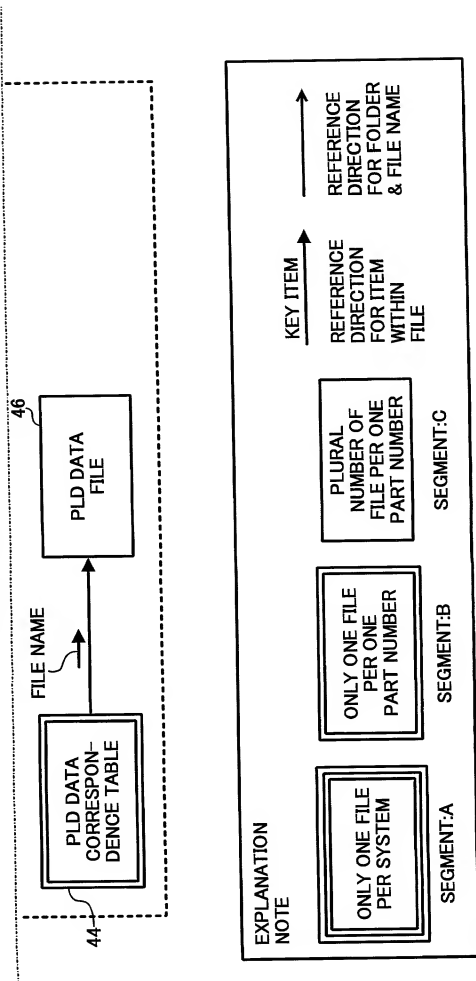


FIG. 5

TEST01

MACR0001

REGWR

t01-01	CMD_REGWR	P01_01	P01_02	P01_03	P01_04
t01-02	CMD_REGWR	P01_01	P01_05	P01_06	P01_07
t01-02_1	REG_CLR	P01_01	P01_08		
t01-03	CMD_REGWR	P01_01	P01_09	P01_10	P01_11
t01-04	CMD_WAIT	P01_12			
t01-05	CMD_REGWR	P01_01	P01_13	P01_14	P01_15
t01-06	CHK_CONNECT	P01_16	P01_17	P01_18	P01_19
t01-07	CHK_DATA	P01_21	P01_22	P01_24	P01_24
t01-08	CMD_REGWR	P01_01	P01_26	P01_29	P01_29

EXAMPLE: EDITOR SECTION GUI ①

FIG. 6

test01 PCB SET TARGET READY

test02 POWER +5V

test03 CPU OUTPUT PO

test04 CLOCK:CPUCCLK

1 test01 PCB SET, TAR

2 test02 POWER +5V

3 test03 POWER +5V

EXAMPLE: EDITOR SECTION GUI ②

FIG. 7

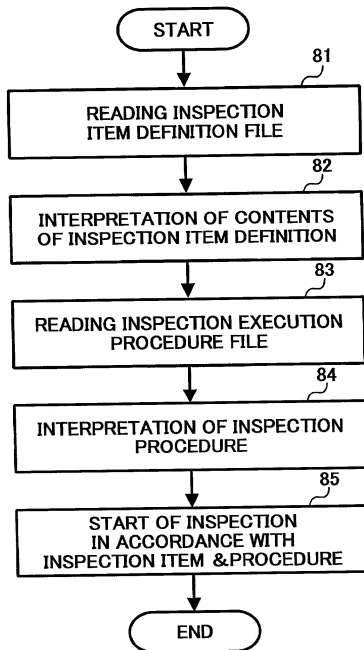


FIG. 8

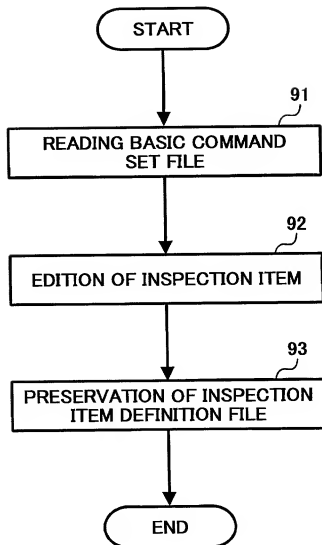


FIG. 9

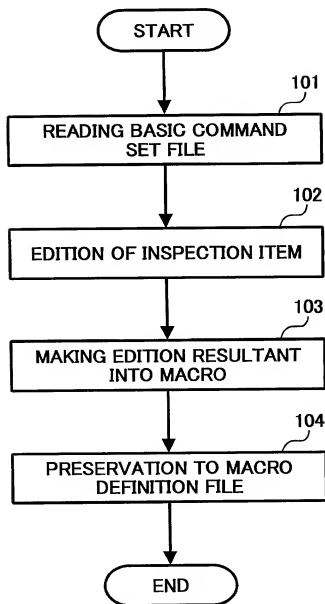


FIG. 10

Select PCB

12345678	SEAHORSE IPU	D:\APIS\SEAHORSE	▲
87654321	ORCA SIPU	D:\APIS\ORCA	▼

EXAMPLE: PART NUMBER SELECTION GUI

FIG. 11A

FIG. 11	FIG. 11A
	FIG. 11B
	FIG. 11C

ITEM NAME	MODEL	BYTE NUMBER	REMARKS
HEADER SECTION			
FILE ID	BYTE[]	23	"BASIC COMMAND SET@HANKS"
SEGMENTATION CHARACTER	BYTE	1	0x02C(',')
VERSION	BYTE[]	8	"V01.00"
SEGMENTATION CHARACTER	BYTE[]	2	0x0D,0x0A
COMMAND NUMBER	BYTE[]	3	001 ~ 999
SEGMENTATION	BYTE[]	2	0x0D,0x0A
SEGMENTATION	BYTE[]	2	0x0D,0x0A
INTERMEDIATE TOTAL		41	
DATA SECTION			
BASIC COMMAND			
INTERMEDIATE TOTAL			
TOTAL			

EXAMPLE: BASIC COMMAND FILE FORMAT

FIG. 11B

BASIC COMMAND FORMAT

COMMAND	PARAMETER 1	PARAMETER 2	
COMMAND (A)	P1(B)	P2(B)	...

COMMANDS & PARAMETERS ARE
 COMPARTMENTALIZED BY [;] WHEN
 SEMI-COLON IS PUT AT BEGINNING, IT IS
 TREATED AS COMMENTS.

A: PARAMETER NUMBER INCLUDED IN COMMAND

B: PARAMETER INFORMATION FORMAT(b1:b2:b3:....)

b:1 PARAMETER TYPE

1: NUMERICAL VALUE

2: LETTER STRING

3: WRITE REGISTER NAME

4: READ REGISTER NAME

5: PROCESSING MODE NAME

6: FILE NAME

b:2 MINIMUM VALUE

* LETTER STRING NUMBER, WHEN b1=b2

* NOTHING EXISTS SUBSEQUENT TO 3

* FOLLOWING NUMERICAL VALUE ARE ENTERED WHEN b1=7

1: CH1 2: CH2 3: EXT 4: SERIAL SELECTION

* ONE OF OPTIONS WHEN b1=8

* COLON(:) IS ENTERED BETWEEN b1 & b2 WHEN THIS PARAMETER DOES NOT EXIST

b:3 MAXIMUM VALUE

* NOTHING EXISTS AFTER b1=2

* 8 IS ONE OF OPTIONS

EXAMPLE: BASIC COMMAND FILE FORMAT

FIG. 11C

USAGE EXAMPLE

COMMAND RESPONDING TEST ID TRANSMISSION TO TARGET
T_TESTID(3),testid(2:3),rwait(1:1:99999)

WRITE COMMAND TO PLTS
P_REGWR(3),wname(3),wrdt(1:0:255)

EXAMPLE: BASIC COMMAND FILE FORMAT

FIG. 12

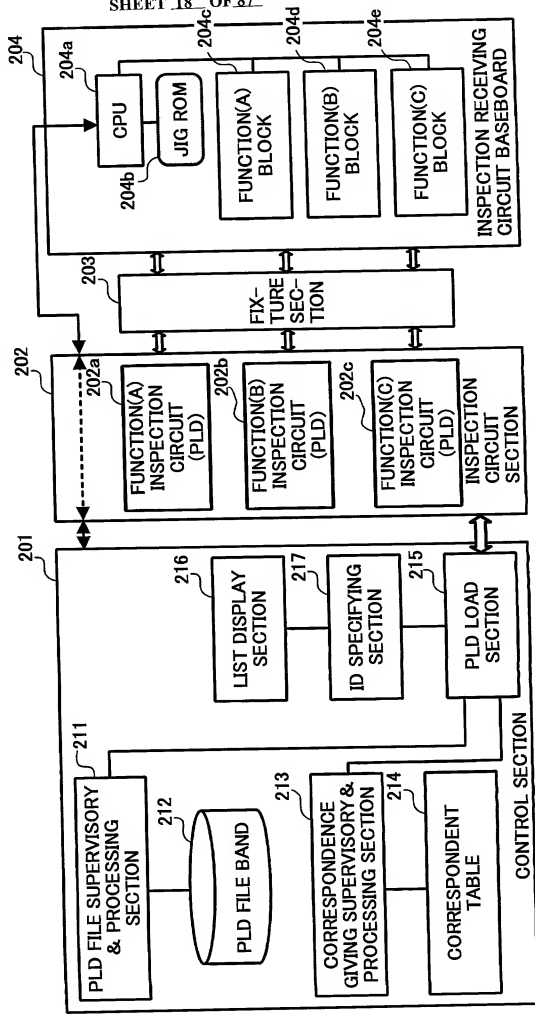


FIG. 13

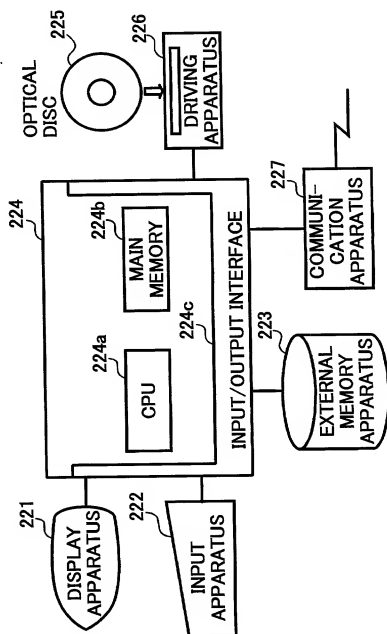


FIG. 14

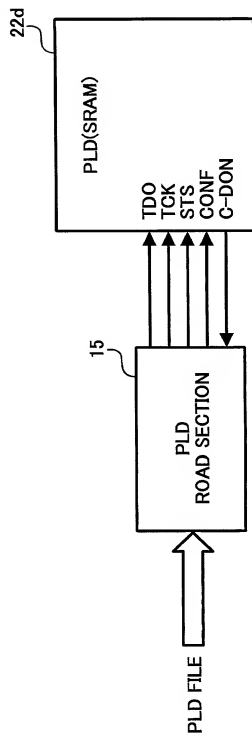
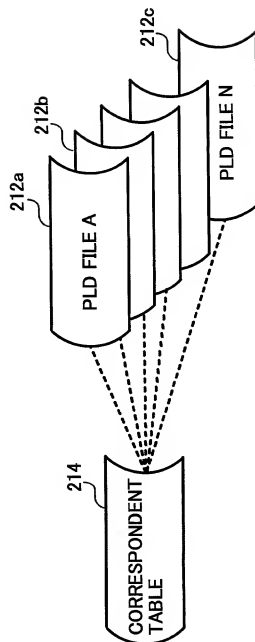


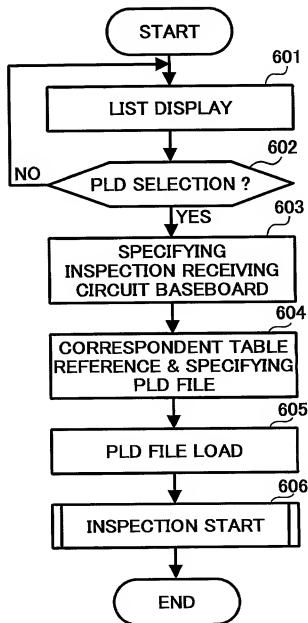
FIG. 15



16a

CANCEL

FIG. 17



10015800-121201

FIG. 18

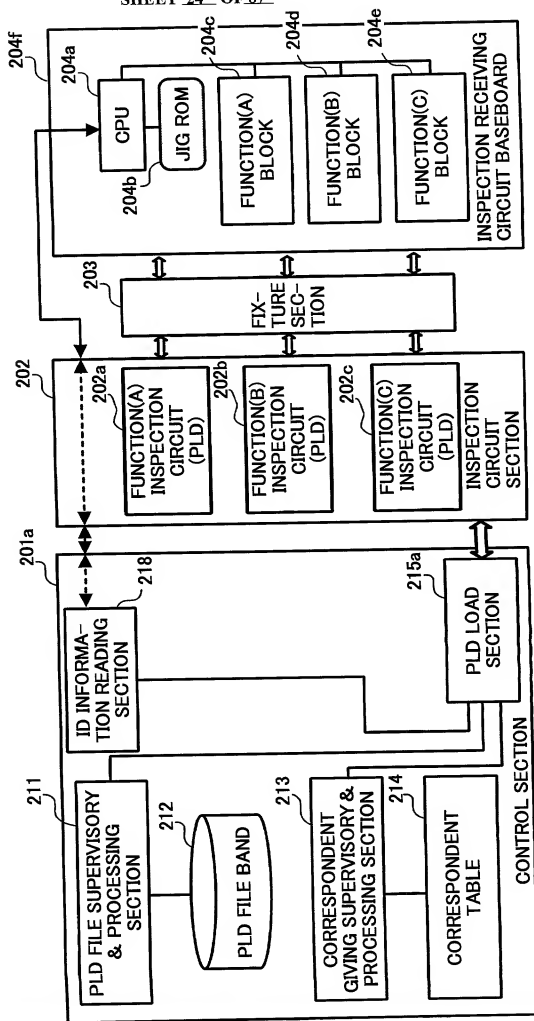


FIG. 19

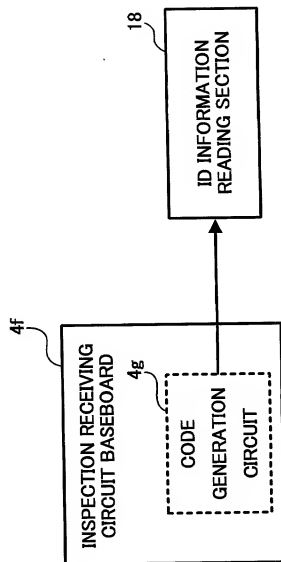
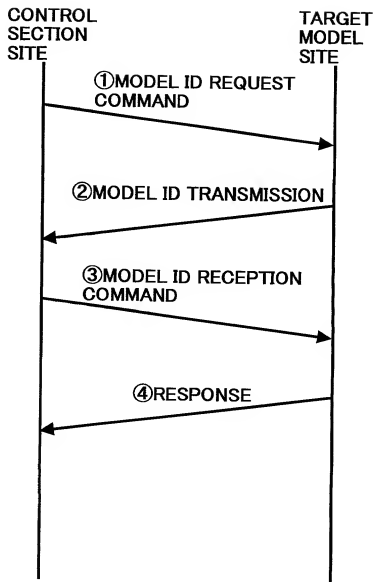


FIG. 20



40045866-24794

FIG. 21

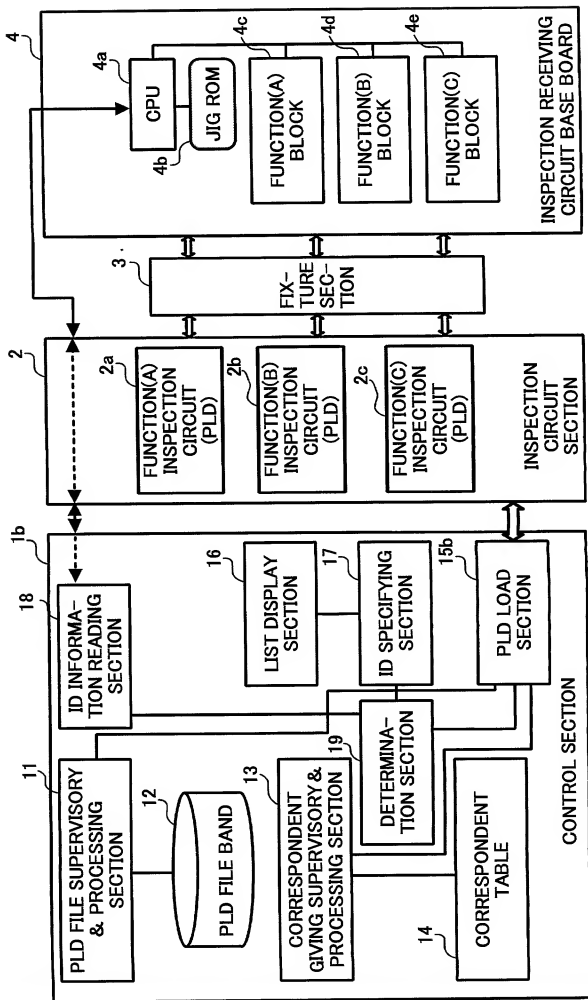


FIG. 22

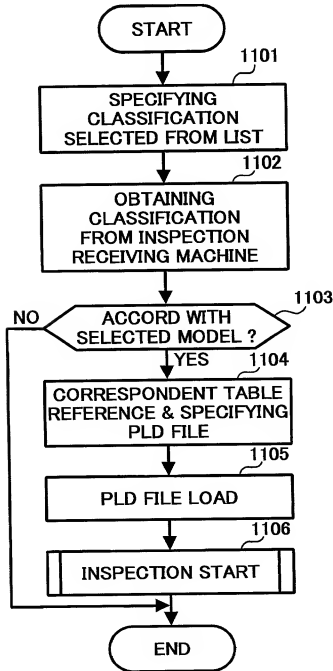


FIG. 23

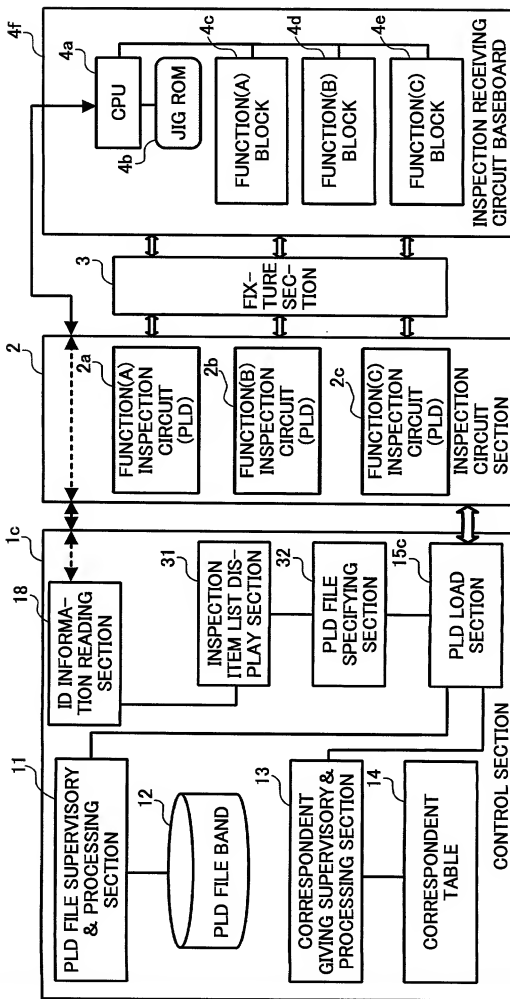


FIG. 24

31b

PLD LOAD

MAIN	SUB
<input type="checkbox"/> PLD_TAL	
<input type="checkbox"/> MPLD1	<input type="checkbox"/> SPLD1
<input type="checkbox"/> MPLD2	<input type="checkbox"/> SPLD2
<input type="checkbox"/> MPLD3	<input type="checkbox"/> SPLD3
<input type="checkbox"/> MPLD4	<input type="checkbox"/> SPLD4
<input type="checkbox"/> MPLD5	<input type="checkbox"/> SPLD5
<input type="checkbox"/> MPLD6	<input type="checkbox"/> SPLD6
<input type="checkbox"/> MPLD7	<input type="checkbox"/> SPLD7
<input type="checkbox"/> MPLD8	<input type="checkbox"/> SPLD8
<input type="checkbox"/> MPLD9	<input type="checkbox"/> SPLD9
<input type="checkbox"/> MPLD10	
<input type="checkbox"/> MPLD11	
<input type="checkbox"/> MPLD12	
<input type="checkbox"/> MPLD13	

CHECK ALL

START(F5)

CLOSE

FIG. 25

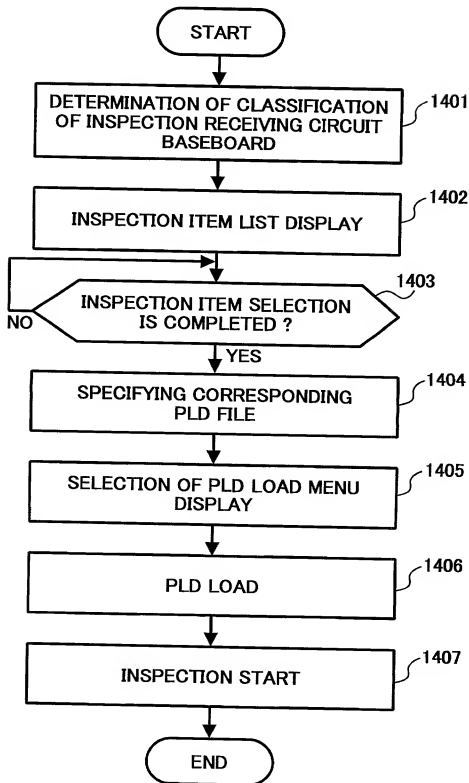


FIG. 25

FIG. 26

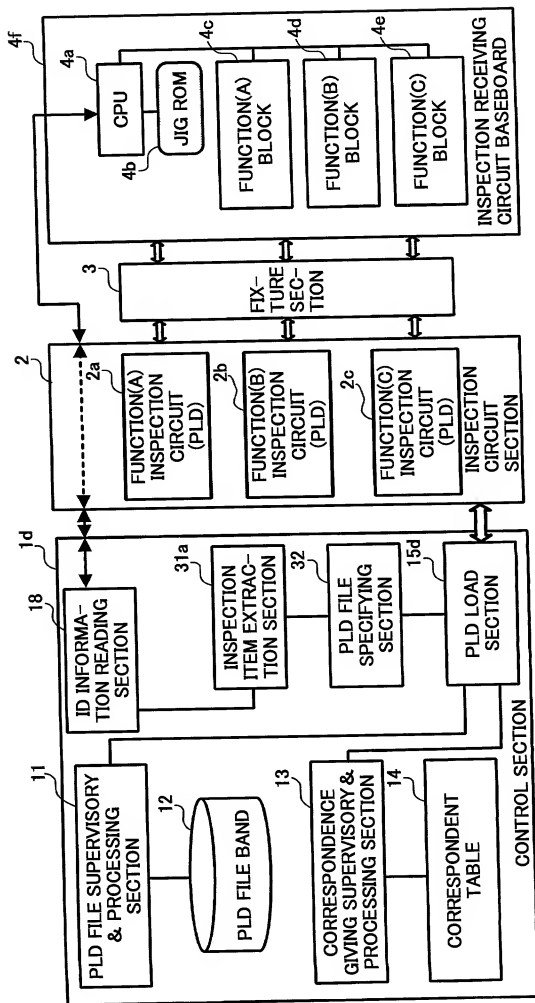


FIG. 27

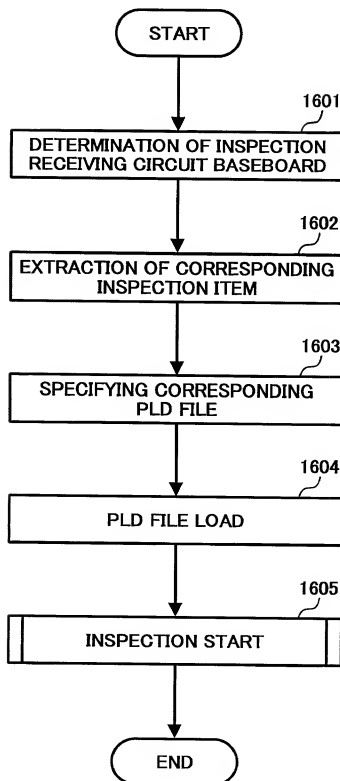


FIG. 28

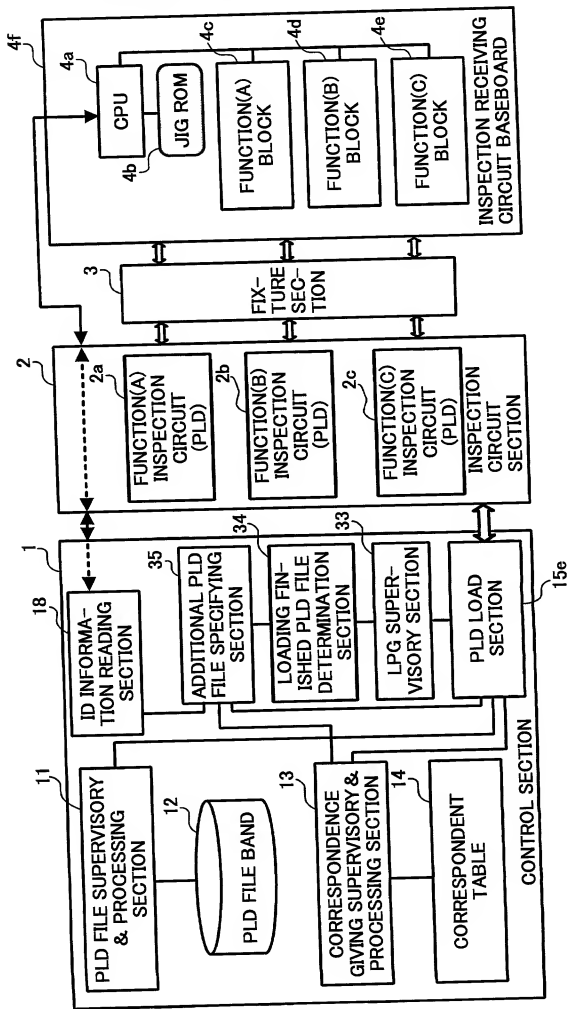
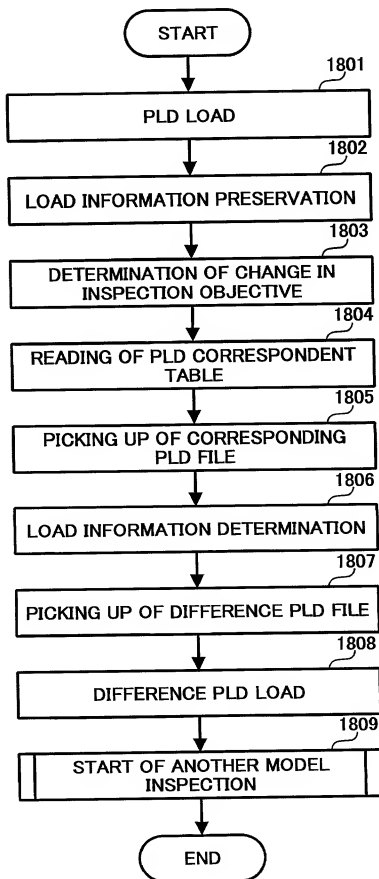


FIG. 29



20250912 12:20:44

FIG. 30

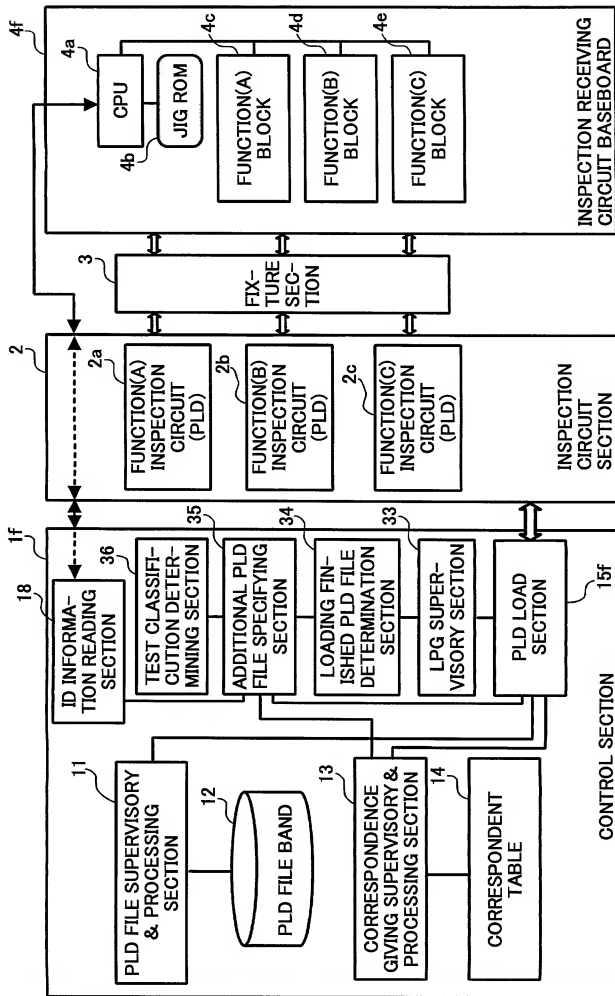


FIG. 31

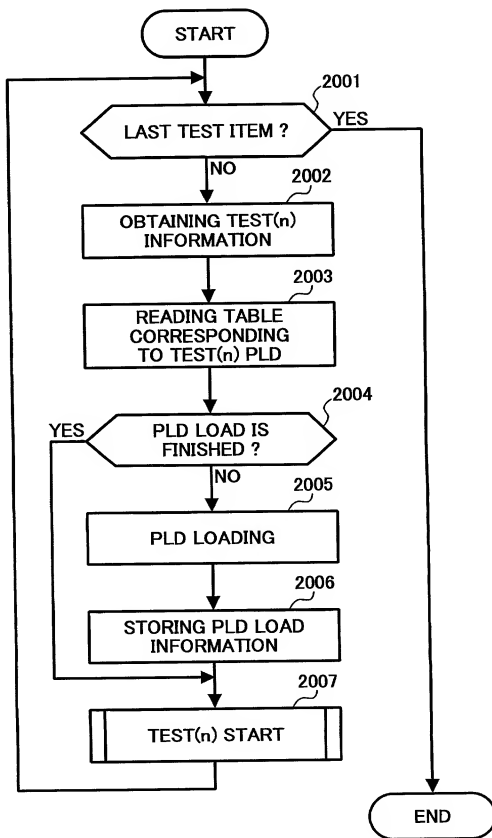


FIG. 32

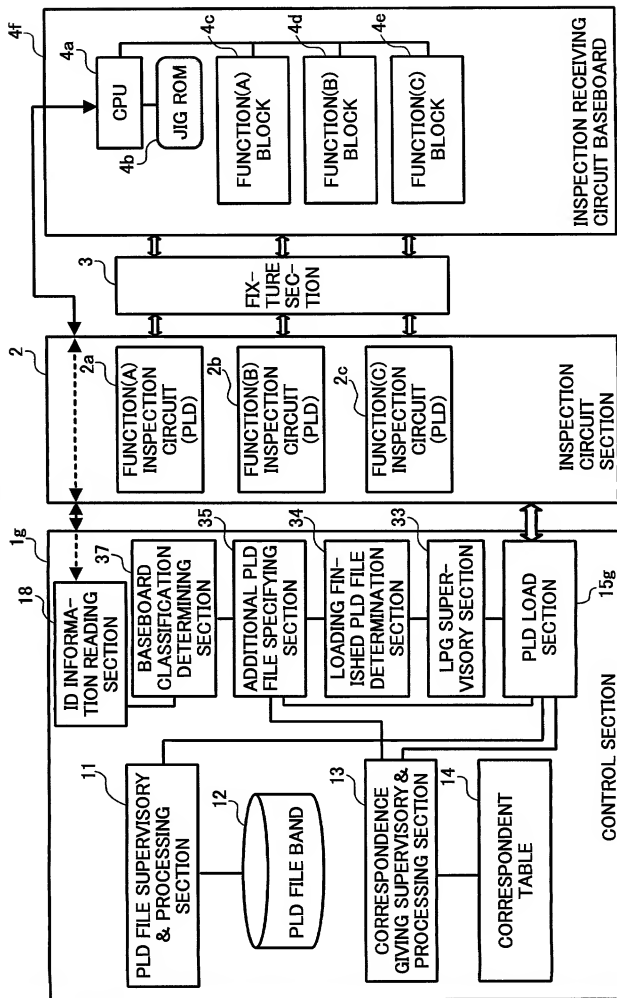


FIG. 33

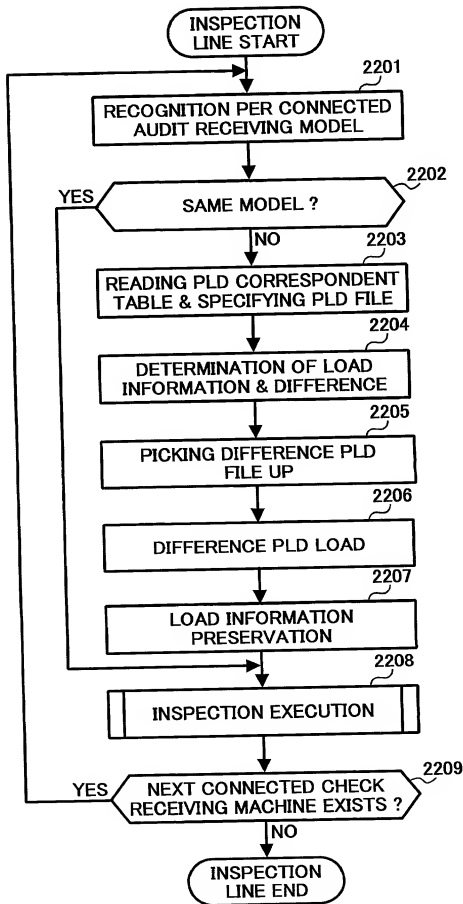


FIG. 34

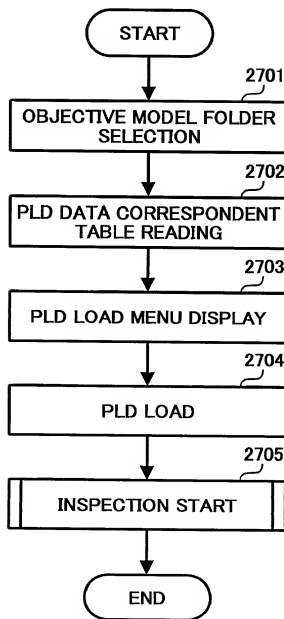


FIG. 35

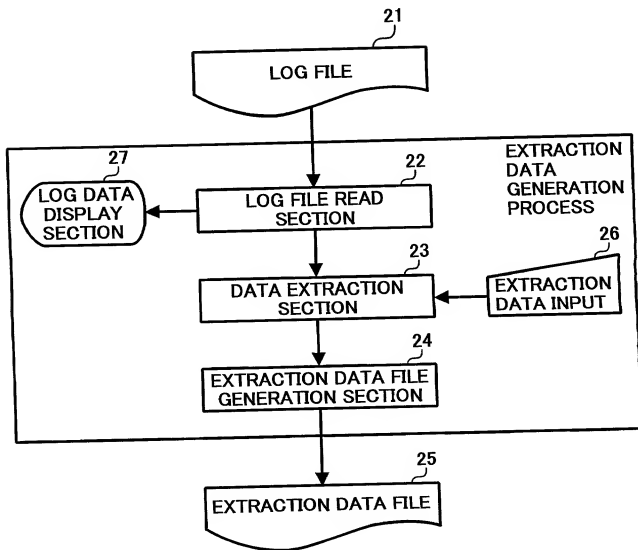


FIG. 36

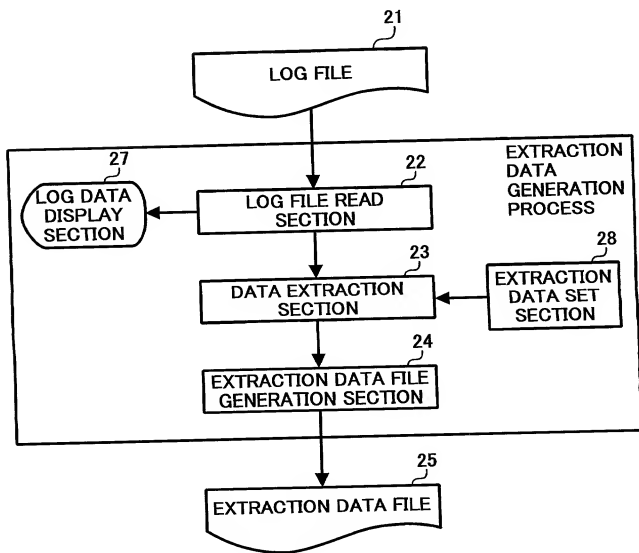


FIG. 37A

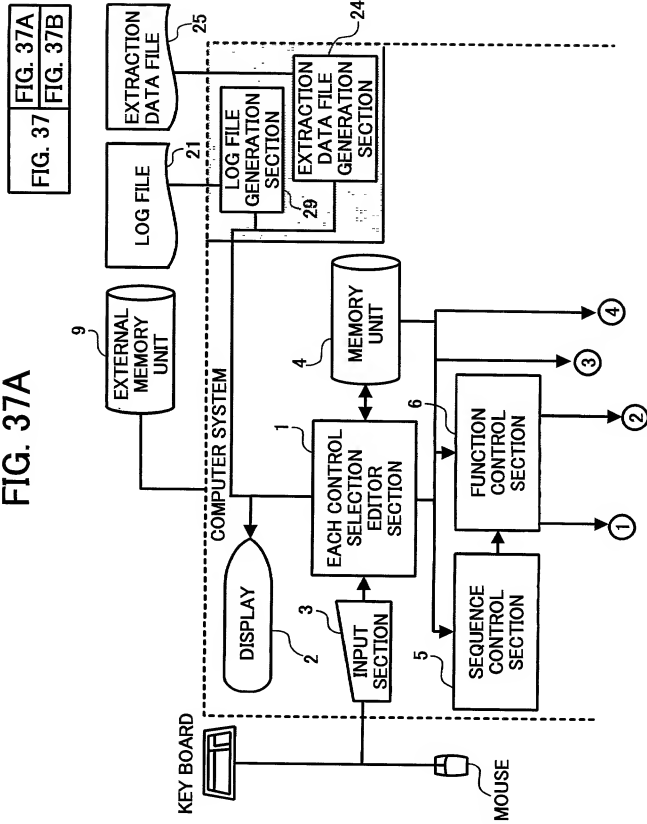


FIG. 37
FIG. 37A
FIG. 37B

FIG. 37B

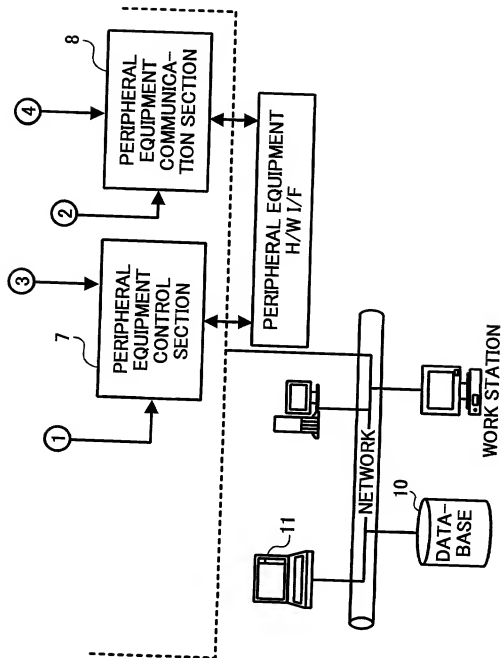


FIG. 38

[illegible]

FIG. 39

PCB	RESULT	NO COUNT	TESTS	DATA
PCB 1	PASS		2,012	0xFF93DA04
PCB 2	PASS		2,010	0xFF93C72B
PCB 3	FILE	3	2,005	0xFF41DB18
PCB 4	PASS		2,011	0xFF93DAF6
PCB 5	PASS		2,010	0xFF93EE1C
PCB 6	PASS		2,013	0xFF93DB28
PCB 7	FILE	5	2,015	0xFFA5C4C2

SELECTION OF COLUMN HAVING EXTRACTION INFORMATION BY CLICKING MOUSE

OK

FIG. 40

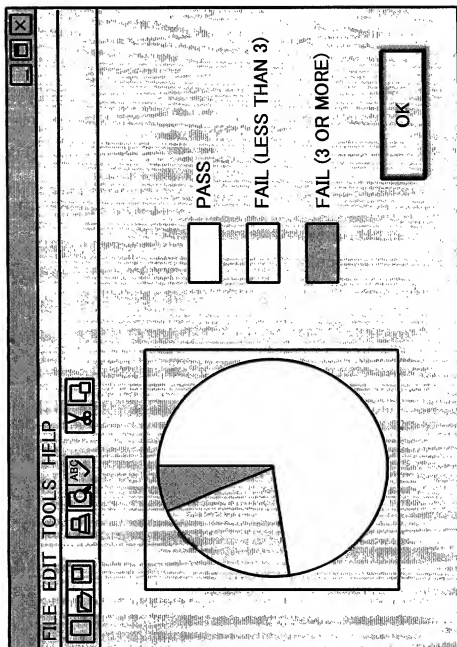


FIG. 41

FIG. 41

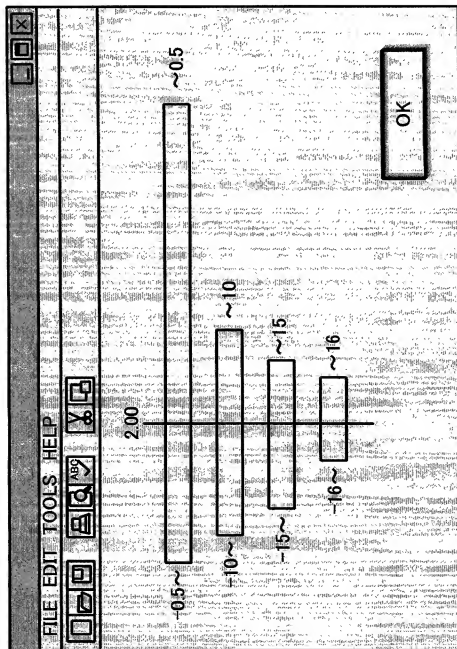


FIG. 42

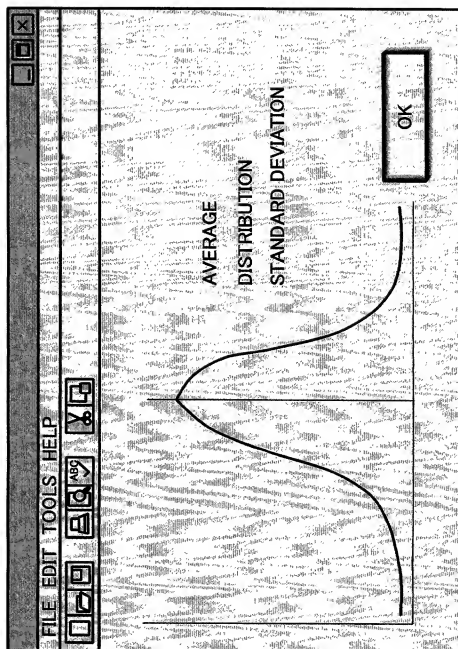


FIG. 43

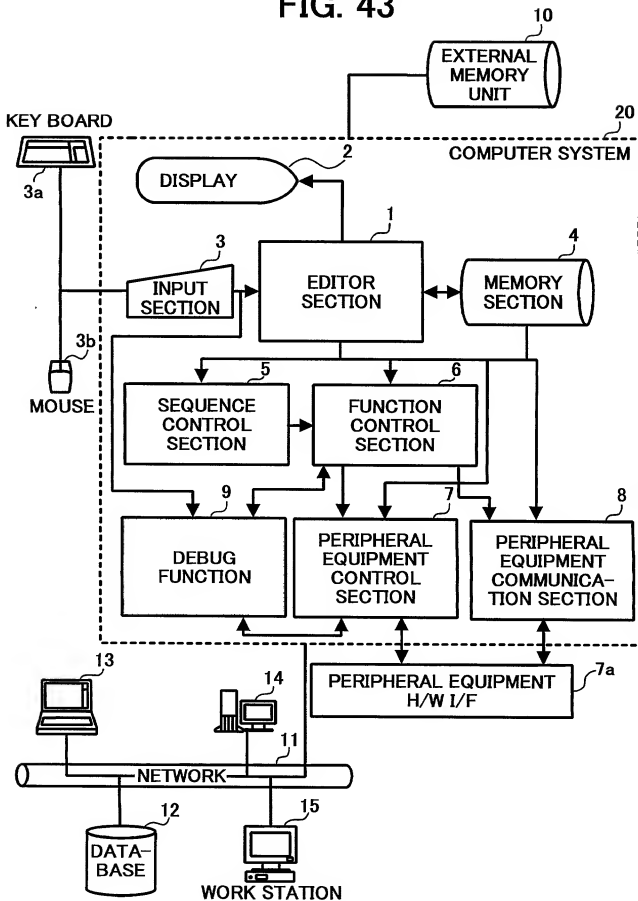


FIG. 44

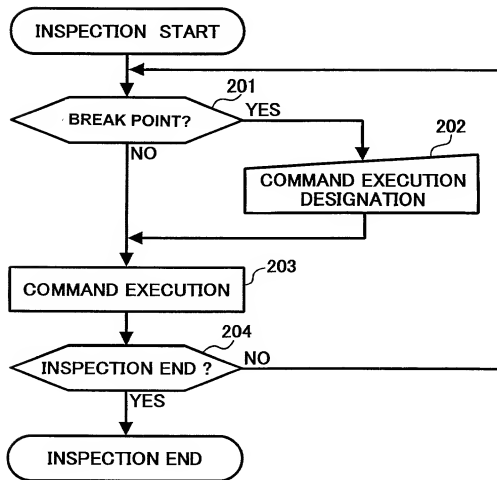


FIG. 45

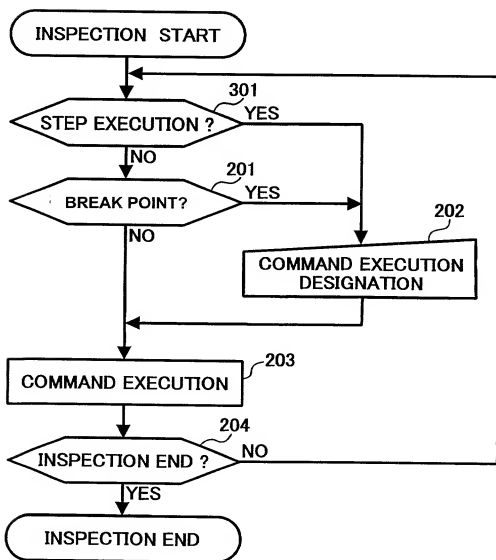


FIG. 46

42
43

MODEL A	PROCESS	INSPECTION	01	03	04	1C	FF	AD	E8	4B		
<input type="checkbox"/>	INSPECTION 1	<input type="checkbox"/>	00000000	ED 93	3D 53	CD F1	3F 30	00000001	ED 93	3D 53	CD F1	3F 30
<input type="checkbox"/>	COMMAND 1	<input type="checkbox"/>	00000002	61 44	CC 54	A1 1C	28 0D	00000002	61 44	CC 54	A1 1C	28 0D
<input type="checkbox"/>	COMMAND 2	<input type="checkbox"/>	00000003	00 21	00 FF	56 98	55 AA	00000003	00 21	00 FF	56 98	55 AA
<input type="checkbox"/>	COMMAND 3	<input type="checkbox"/>	00000004	01 00	CC DD	3C 64	2C 00	00000004	01 00	CC DD	3C 64	2C 00
<input type="checkbox"/>	COMMAND 4	<input type="checkbox"/>	00000005	00 00	CC 1C	0D 0A	1A 01	00000005	00 00	CC 1C	0D 0A	1A 01
<input type="checkbox"/>	INSPECTION 2	<input type="checkbox"/>	00000006	00 00	00 1E	6D E8	FF	00000006	00 00	00 1E	6D E8	FF
<input type="checkbox"/>	INSPECTION 3	<input type="checkbox"/>	00000007	FF FF	FD DB	FF FF	32 57	00000007	FF FF	FD DB	FF FF	32 57
<input type="checkbox"/>	INSPECTION 4	<input type="checkbox"/>	00000008	7A AF	33 84	25 18	9C 0F	00000008	7A AF	33 84	25 18	9C 0F
<input type="checkbox"/>	INSPECTION 5	<input type="checkbox"/>	00000009	DF 91	04 BC	1A 0A	FA 89	00000009	DF 91	04 BC	1A 0A	FA 89
<input type="checkbox"/>	INSPECTION 6	<input type="checkbox"/>	0000000A	51 32	67 D2	FF 3E	C9 20	0000000A	51 32	67 D2	FF 3E	C9 20
<input type="checkbox"/>	INSPECTION 7	<input type="checkbox"/>	0000000B	02 00	00 38	4C D5	62	0000000B	02 00	00 38	4C D5	62
<input type="checkbox"/>	INSPECTION 8	<input type="checkbox"/>	0000000C	68 3F	62 69	FB EA	4B 82	0000000C	68 3F	62 69	FB EA	4B 82

F0	00	00	FF	FF	FF
F0	01	30	3C	61	60
F1	31	7A	10	C8	1A
00	00	00	00	00	00
F0	00	F1	03	F9	4E
F0	00	79	DA	1F	2C
F0	21	33	51	6C	FF

FIG. 47

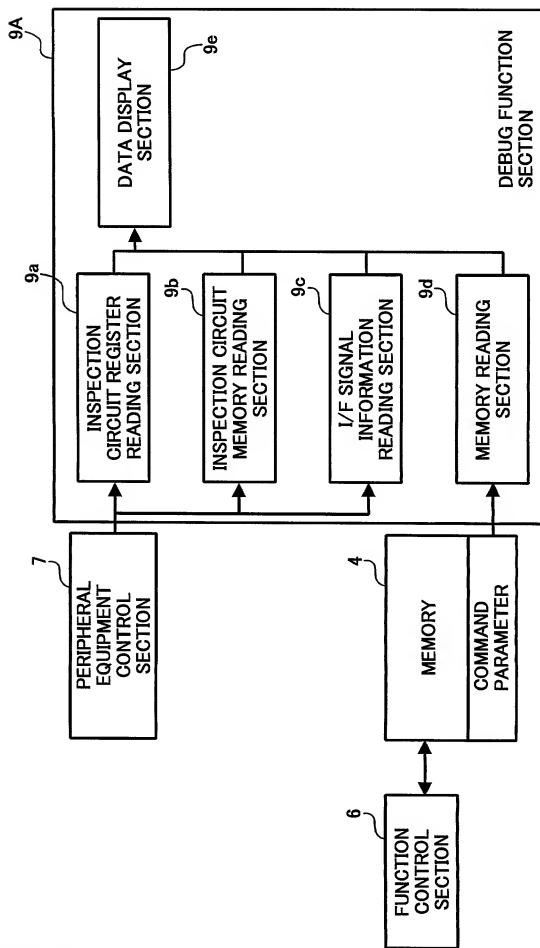


FIG. 48

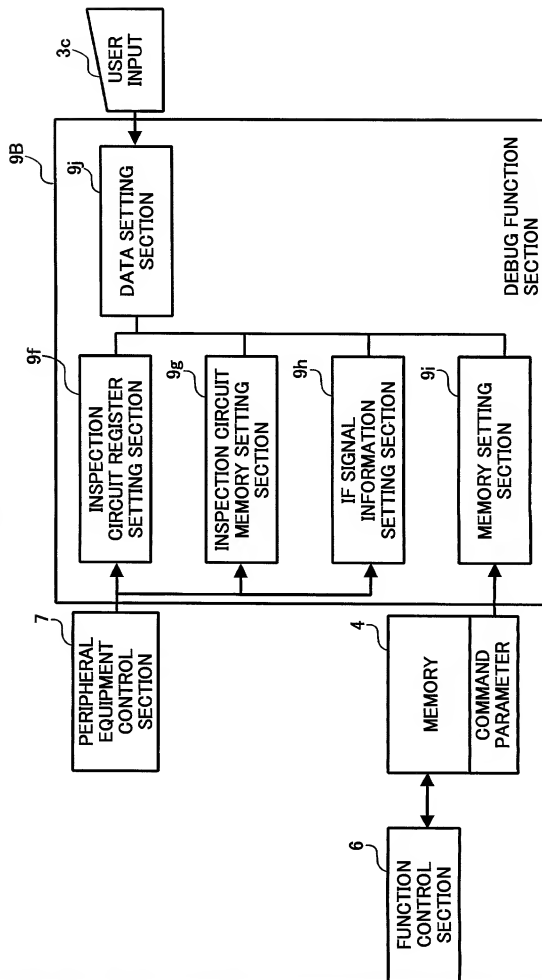


FIG. 49

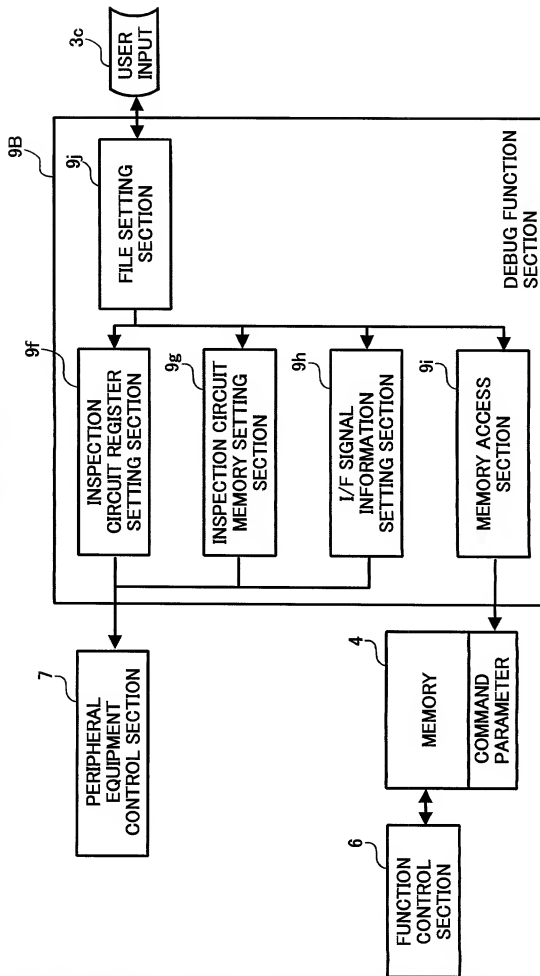


FIG. 50

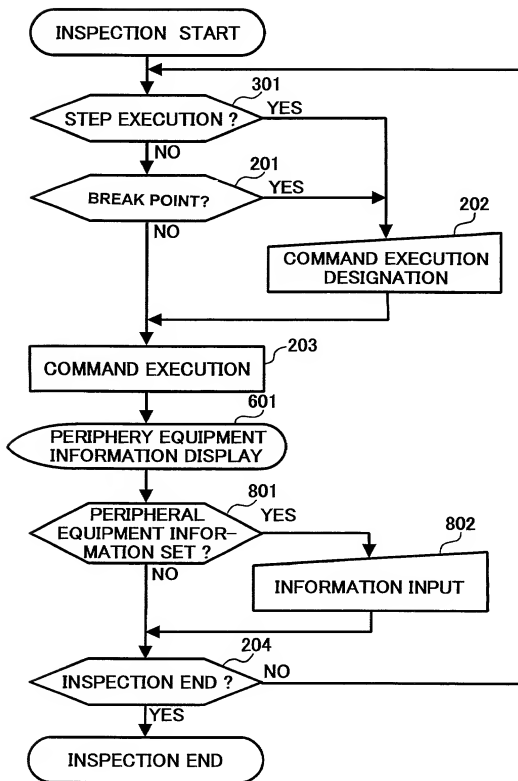


FIG. 51

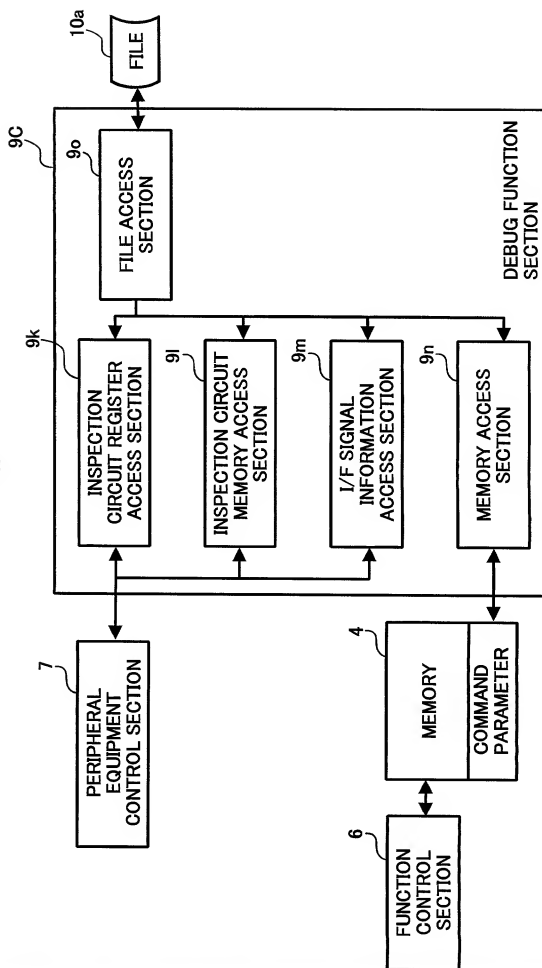


FIG. 52

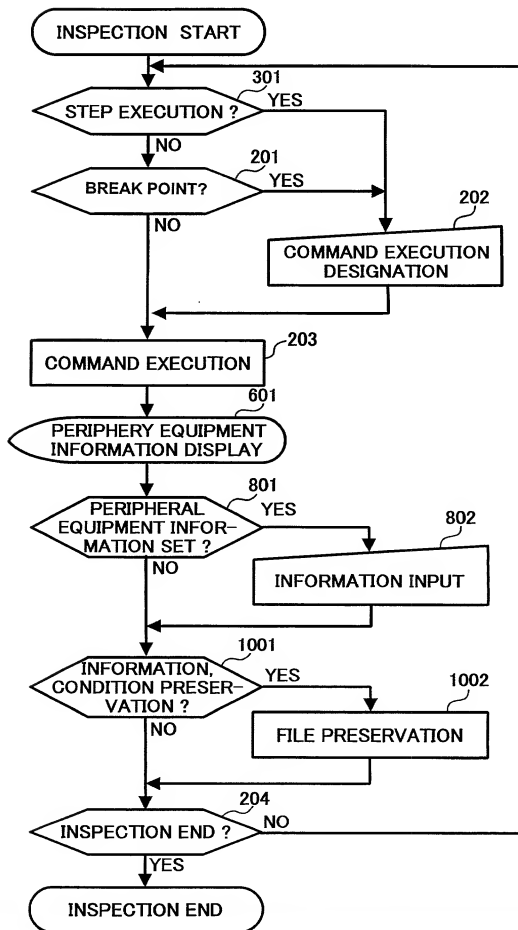


FIG. 53A

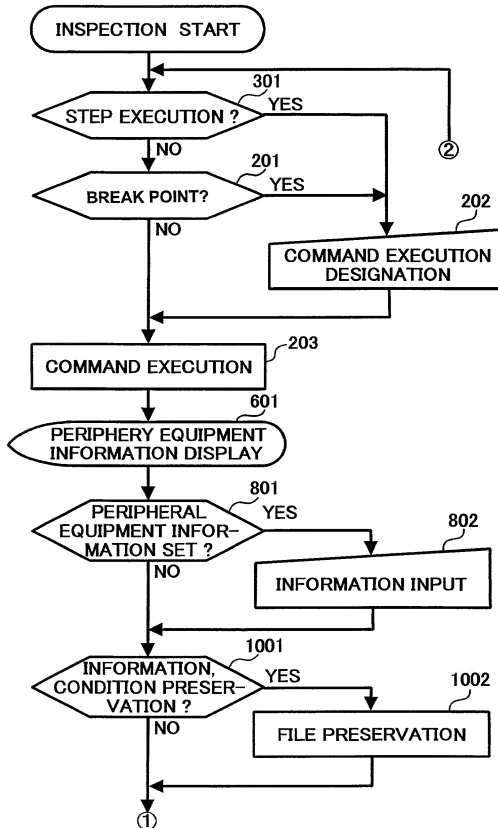


FIG. 53B

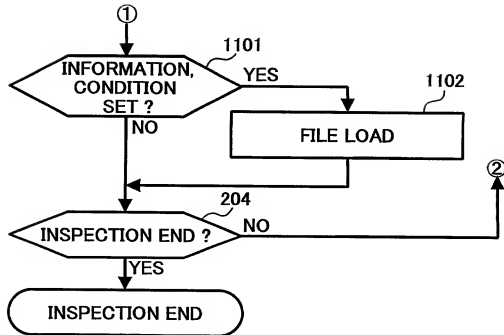


FIG. 54

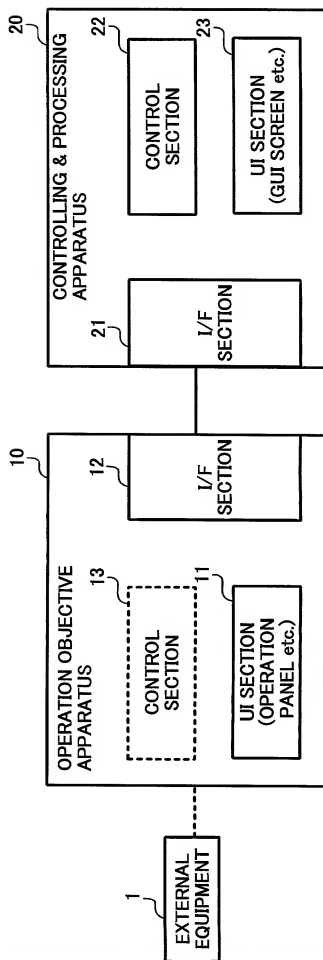


FIG. 55A

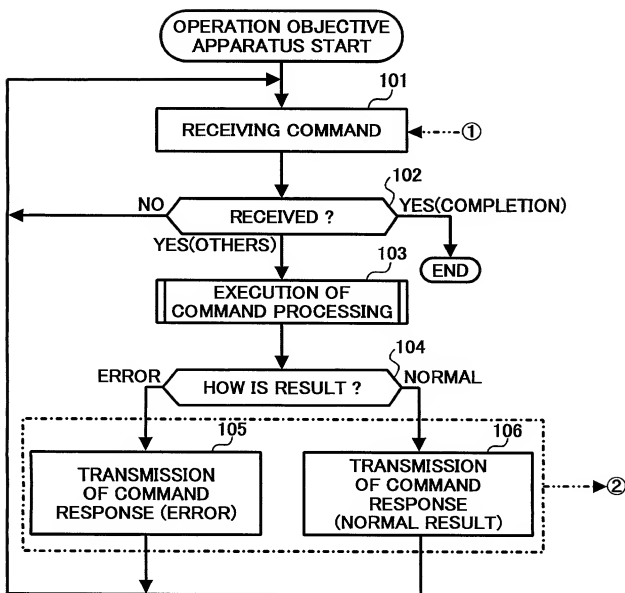


FIG. 55B

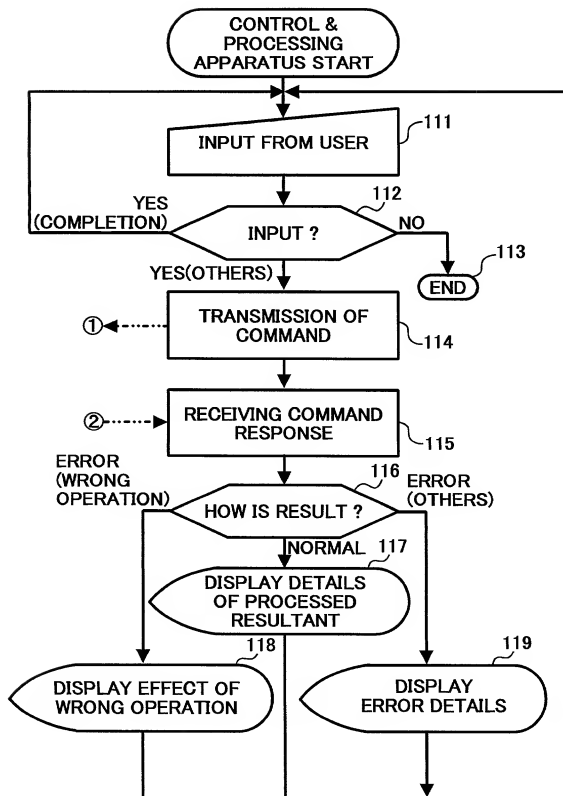


FIG. 56A

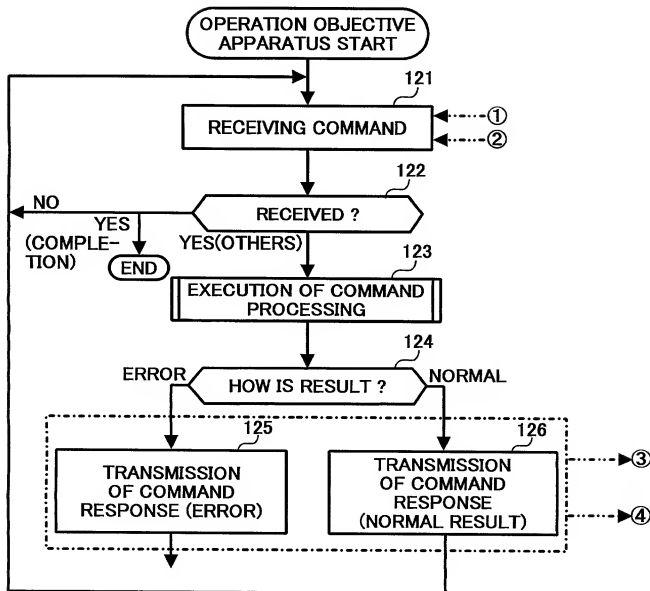


FIG. 56B

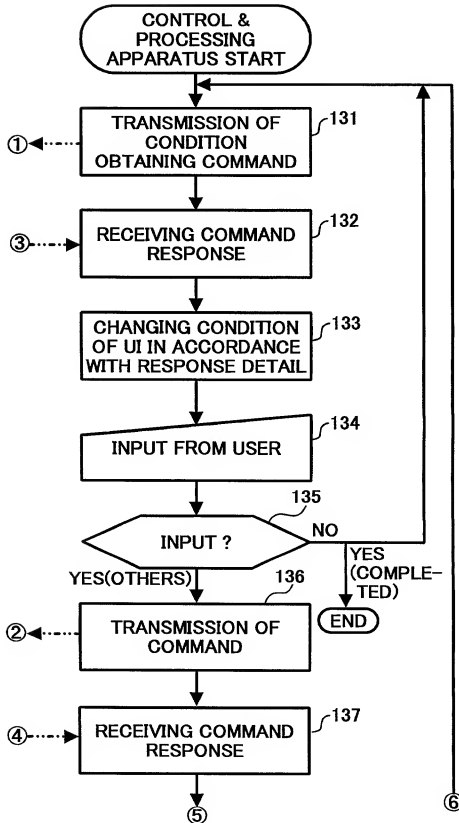


FIG. 56C

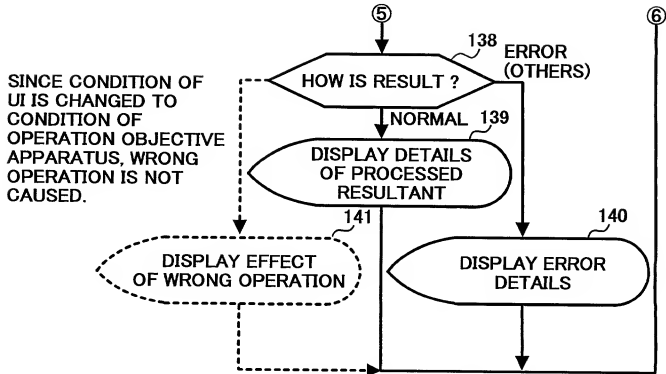


FIG. 57A

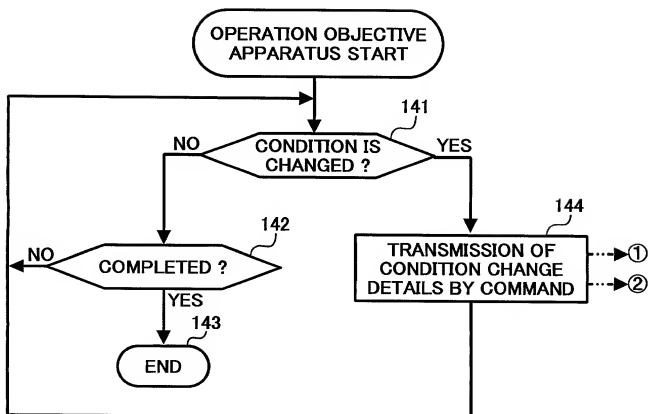
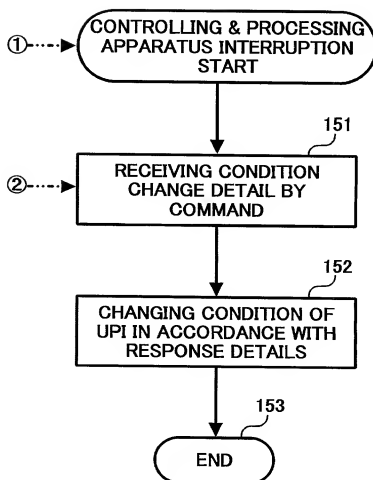


FIG. 57B



10015889.121701

FIG. 57C

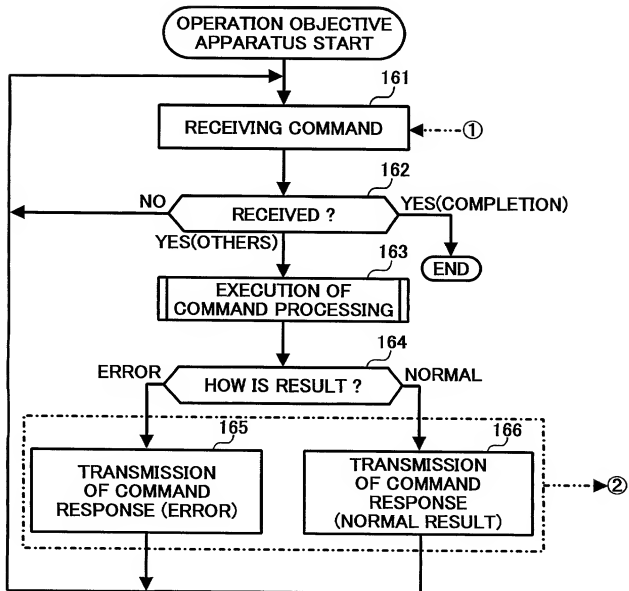


FIG. 57D

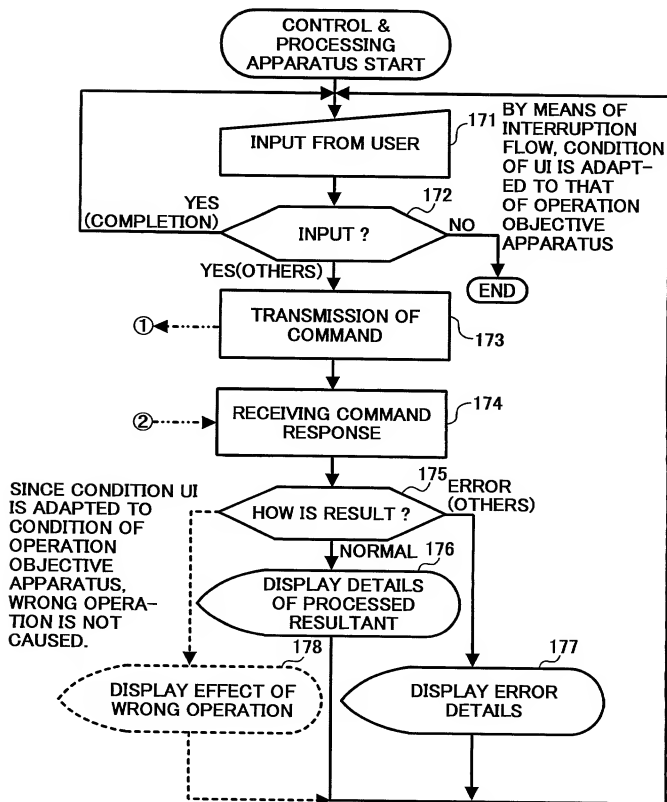


FIG. 58A

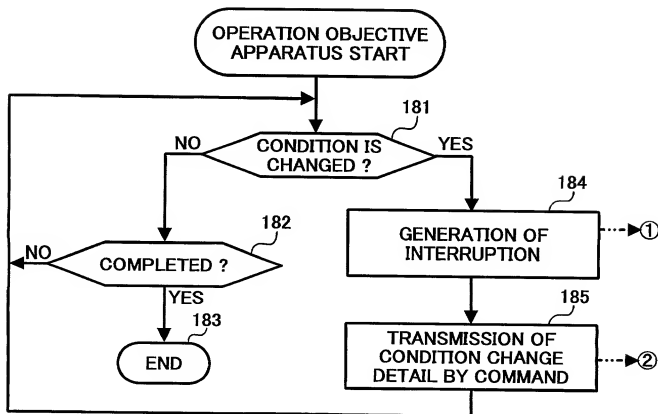


FIG. 58B

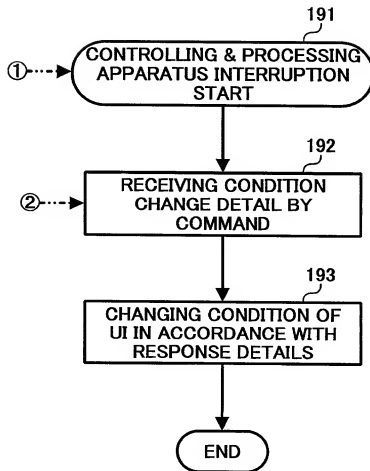


FIG. 58C

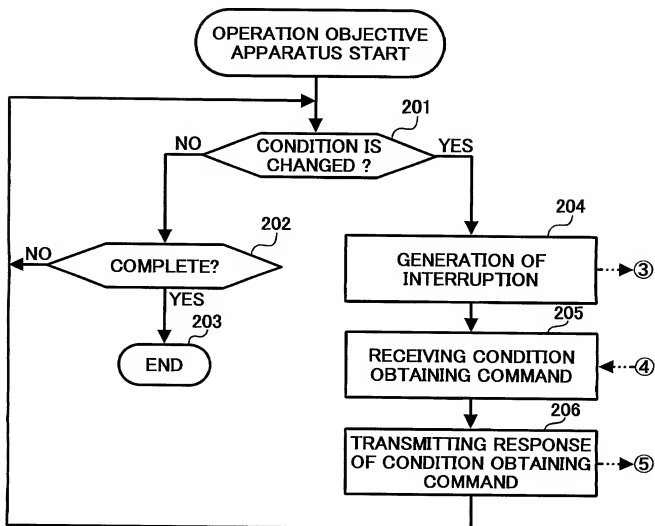
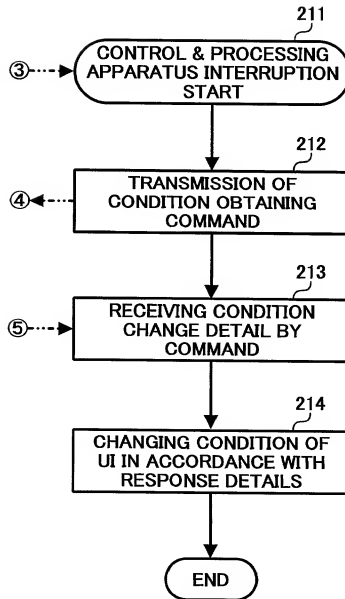


FIG. 58D



10015889.121701

FIG. 59A

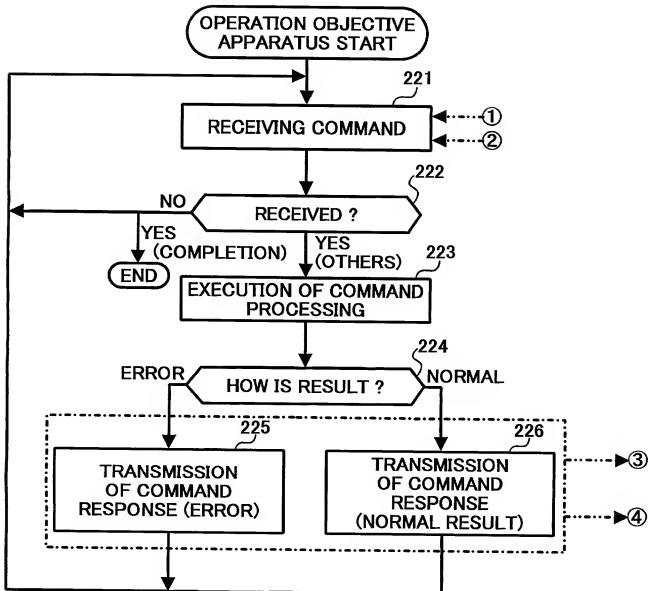
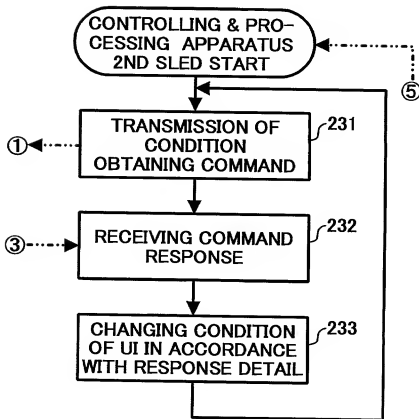
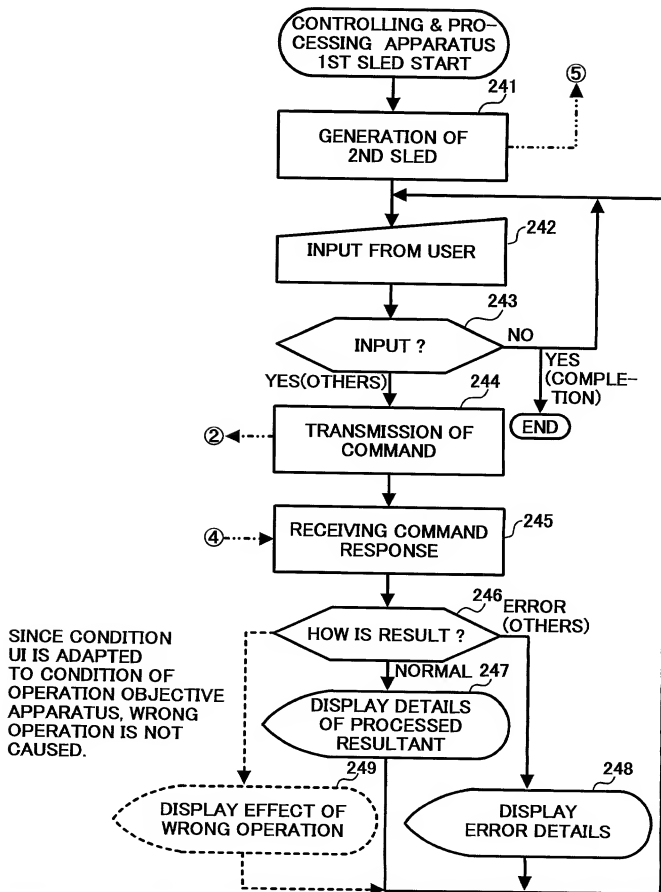


FIG. 59B



10015389-137791
10/25/00 08:51:07

FIG. 59C



1005000 12701

FIG. 60A

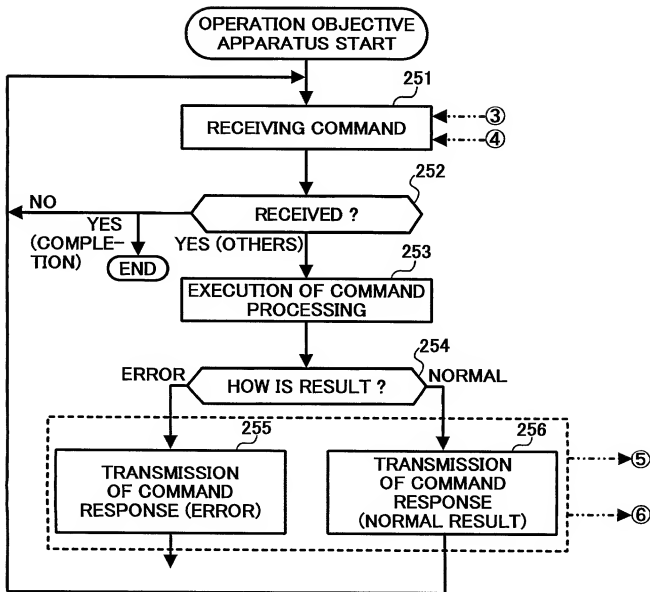


FIG. 60B

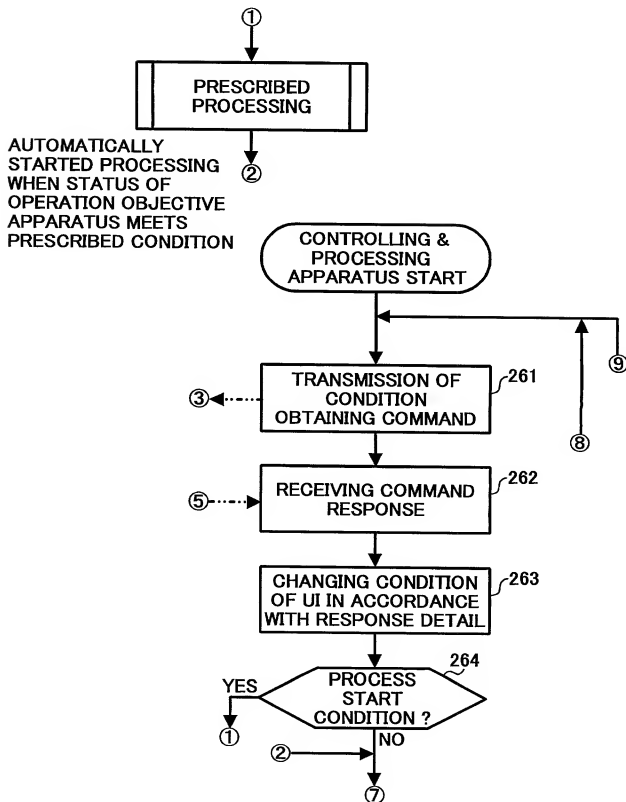


FIG. 60C

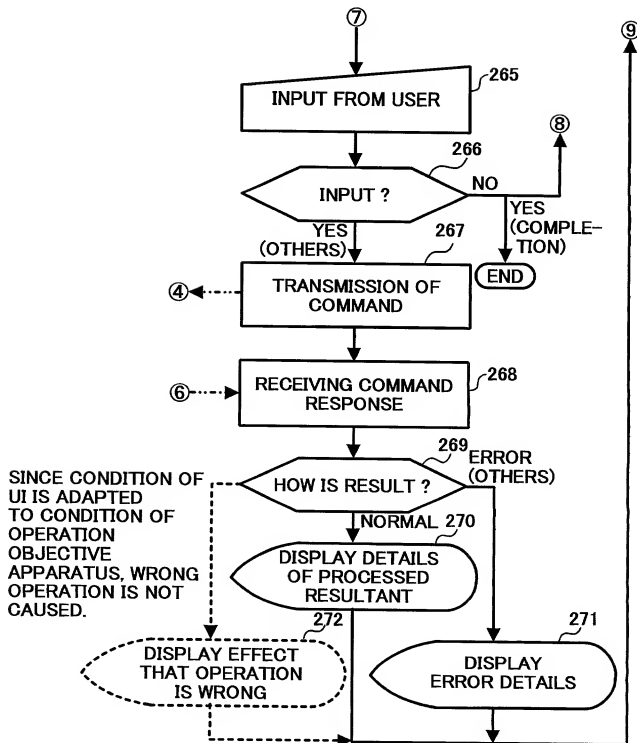


FIG. 61A

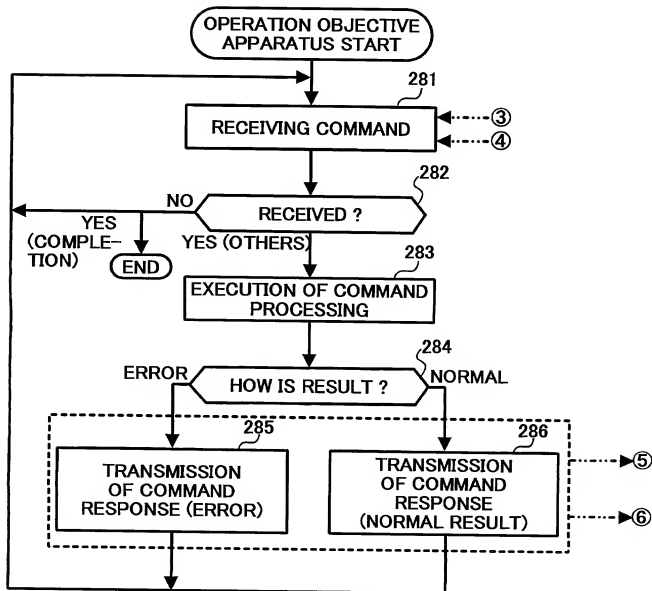


FIG. 61B

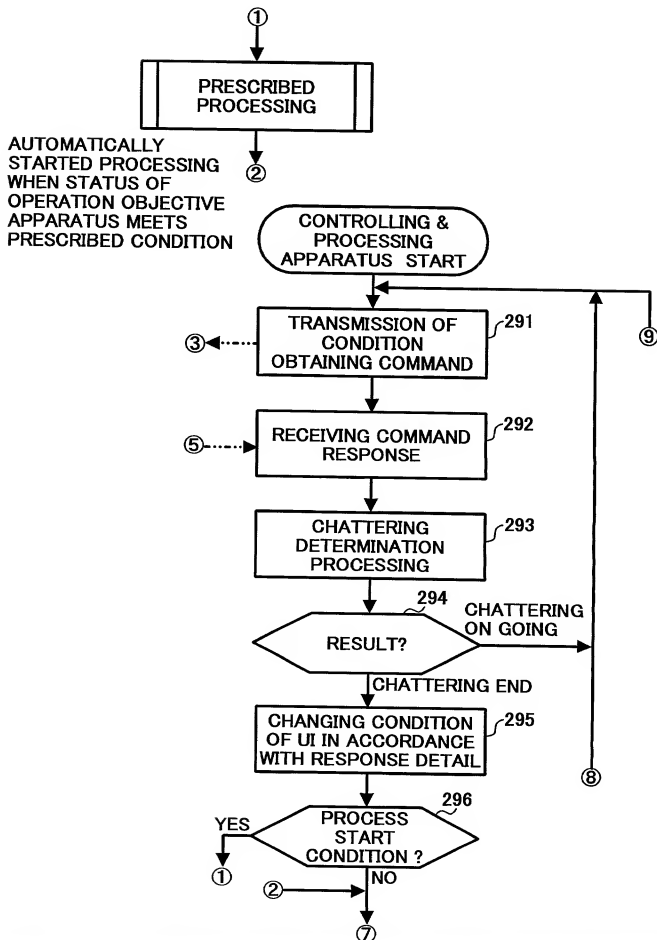


FIG. 61C

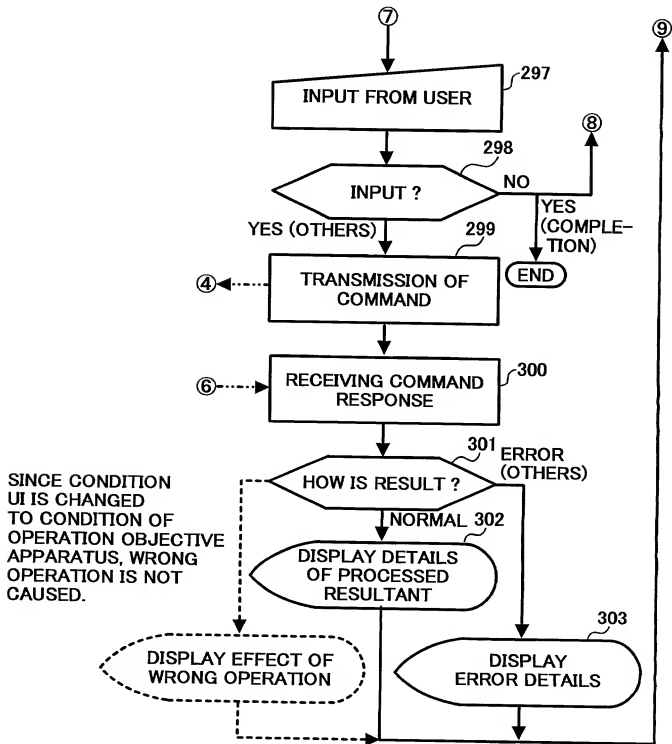


FIG. 62A

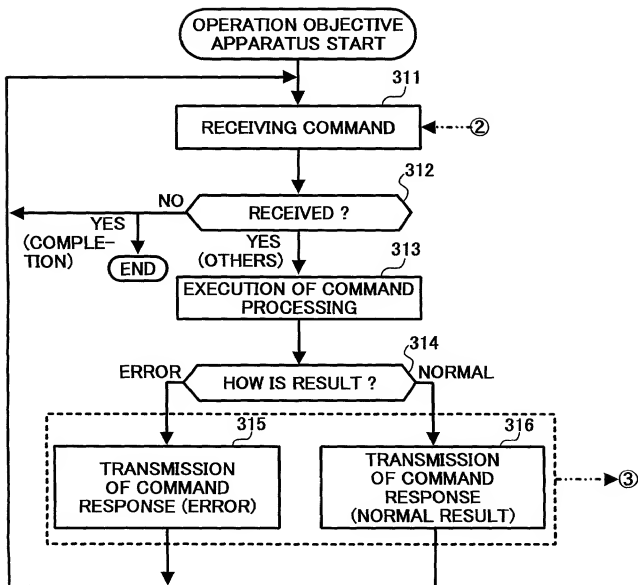


FIG. 62B

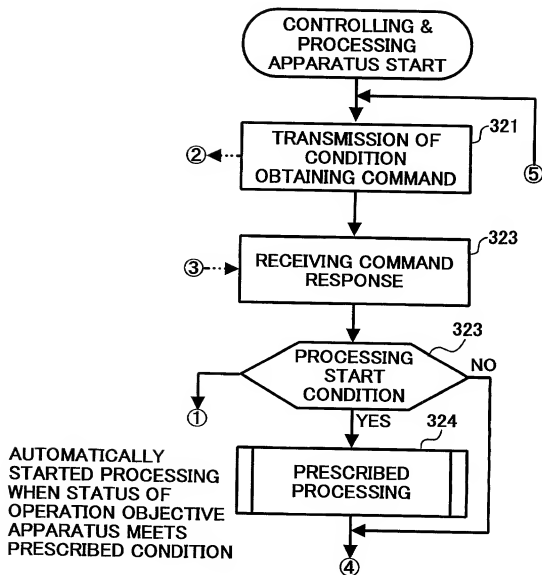


FIG. 62C

